

**RAJEEV GANDHI MEMORIAL COLLEGE OF ENGG. & TECH. (Autonomous)**  
Nandyal, A.P.

**END EXAMINATION SCHEDULE**

**III B.Tech II Semester (R20) Regular End Examinations (Autonomous) - May - 2024**  
**Timings: 1:30 PM To 4:30 PM**

Date / Branch	Civil Engineering	Electrical & Electronics Engineering	Mechanical Engineering	Electronics & Communication Engineering	Computer Science & Engineering	Computer Science & Engineering (Data Science)	Computer Science & Engineering & Business Systems
20-06-2024 Thursday	Design of Steel Structural Elements (A0121206)	Basics of Signals & Systems (A0219206)	Design of Transmission Elements (A0320206)	Digital Signal Processing (A0423206)	Software Testing Methodologies and Tools (A0534206) <b>Common to CSE &amp; CSE DS</b>		Computer Networks (A0516205)
		Electrical Machines - III (A0212205)(RA)					
22-06-2024 Saturday	Environmental Engineering (A0122206)	Power System Analysis (A0220206)	Dynamics of Machinery (A0321206)	Microwave Engineering and Optical Communication (A0424206)	Computer Graphics (A0535206)	Natural Language Processing (A3210206)	Software Engineering For Business Applications (A3438206)
	Geo informatics (A0112205)(RA)						
25-06-2024 Tuesday	Transportation Engineering (A0123206)	Power System Protection (A0221206)	Refrigeration And Air-Conditioning (A0322206)	Digital Communication (A0425206)	Big Data Analytics (A0536206) <b>Common to CSE &amp; CSE&amp;BS</b>	Deep Learning (A3211206)	Big Data Analytics (A0536206) <b>Common to CSE &amp; CSE BS</b>
27-06-2024 Thursday	Pressurised Concrete (A0126206)	Embedded Controllers (A0420206)	Fundamentals of Java Programming (A0531206)	Computer Organization & Architecture (A0506203)	Android Programming (A0525205)	Big Data (A3212206)	Marketing Research and Marketing Management (A3436206)
29-06-2024 Saturday	Basics of Java Programming (SDC) (A0529206)	Arduino Programming (SDC) (A0422206)	Parametric Modelling-I (SDC) (A0328206)	Digital Design Using Verilog (SDC) (A0429206)	Professional Ethics and Soft Skills (SDC) (A0028206)	Data Visualization and Presentation (SDC) (A3214206)	Business Communication and Value Sciences (SDC) (A3437206)
					Cloud Foundations (SDC) (A0524205)(RA)		
02-07-2024 Tuesday	-----	-----	-----	VLSI Design (A0421206)	-----	-----	-----

**NOTE:** 1) The weightage of internal marks of two mid examinations will be considered as 75% of marks for the best performance and 25% from the remaining mid examination. No choice in the Mid Examinations.  
2) There is no change in the remaining Academic Calendar

**CONTROLLER OF EXAMINATIONS**  
Date: 10-06-2024

*10/6/24*

**PRINCIPAL**  
*10.6.2024*